SRI GVG VISALAKSHI COLLEGE FOR WOMEN

(Autonomous)

Affiliated to Bharathiar University

Re- Accredited at A^{+} Grade by NAAC (Fourth Cycle)

Udumalpet - 642128, Tamilnadu

Report of Webinar on

"Electronic Circuit Design and simulation using falstad"

Organised by Department of Physics

Under DBT Star College Scheme

Platform : Google Meet

Date : 27.05.2021,Friday.

Time:10:30 AM to 12:00 PM

Department of Physics
Sri GVG Visalakshi College for Women

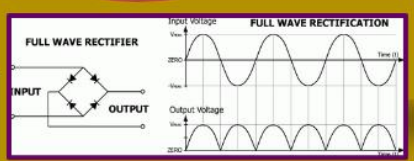
Re-Accredited at A+ Grade by NAAC (4th Cycle)
Udumalpet, Tamilnadu

invites you for the

Workshop organised under DBT Star College Scheme

"Workshop on Electronic Circuit Design and Simulation using Falstad "

DR.RAJU PATHANGANI
ASSOCIATE PROFESSOR OF PHYSICS
GEETHANJALI COLLEGE OF ENGINEERING
HYDERABAD



10.30 AM
27th May 2021

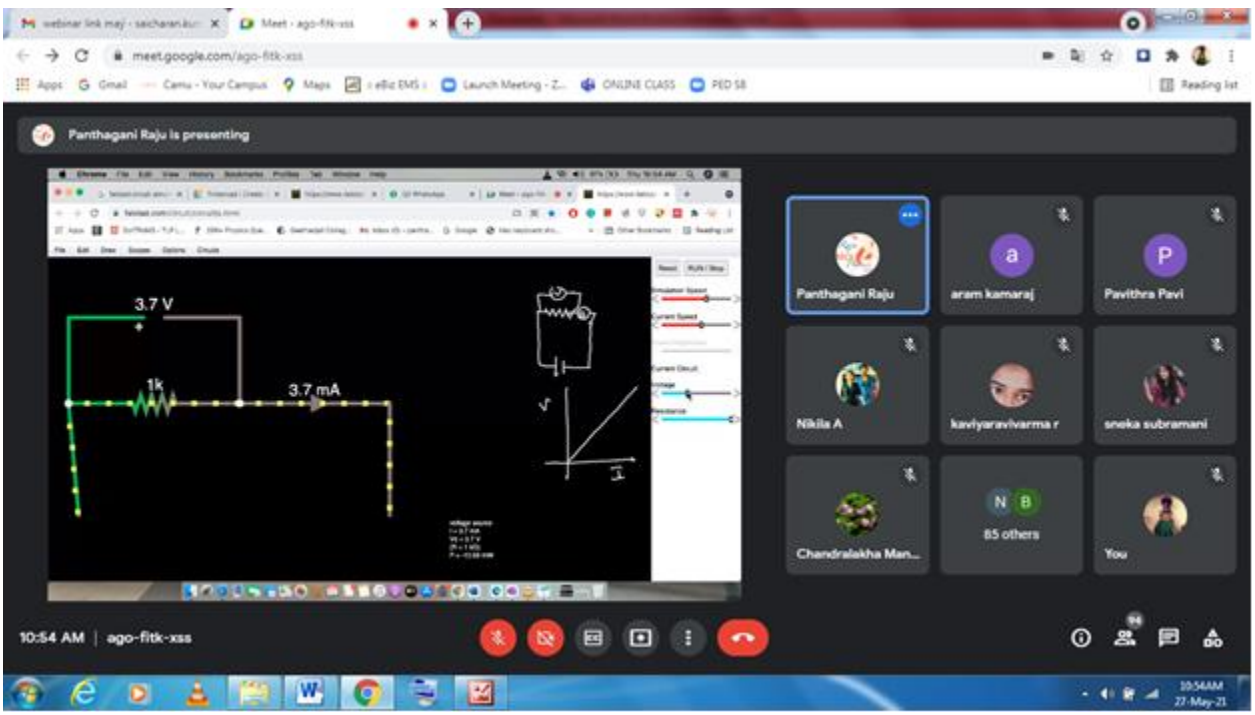
Join us through
Google Meet

Report

The Webinar on Electronic Circuit Design and simulation using falstad was organized by Department of Physics under DBT Star College Scheme on 27.05.2021(FN). In the time of pandemic, COVID-19 outbreak, the educational institutions also kept closed in order to avoid the spread of COVID-19. However, the online platforms are used to conduct theory subject but conduction of laboratory experiments were difficult as the setup is not available and students not in the situation to do the experiments in regular laboratories at the college. In order to engage the students in the academics and make them learn to conduct online experiments Virtual Labs and its simulation is an excellent platform.

The session started with welcoming the resource person from Dr.Raju Pathangani, Associate Professor of Physics, Geethanjali College of Engineering, Hyderabad to the webinar. The resource person started the session by giving introduction about falstad platform. He showed live demonstration of electronic experiments such as Ohm's Law, Characteristics of PN junction diode, Zener Diode, LED characteritics, LCR circuit,Half wave & Full wave rectifiers and rectifier circuit using transistor. He demonstrated how to design a circuit using virtual components, how to draw the graph and simulation process. Students are actively participated the webinar and interacted with the resource person. The webinar was ended with vote of thanks by Sindhu, III B.Sc. Physics student. There are 100 participants were benefited from this webinar.

Outcome: students are able to design and simulate the electronic experiments in falstad platform.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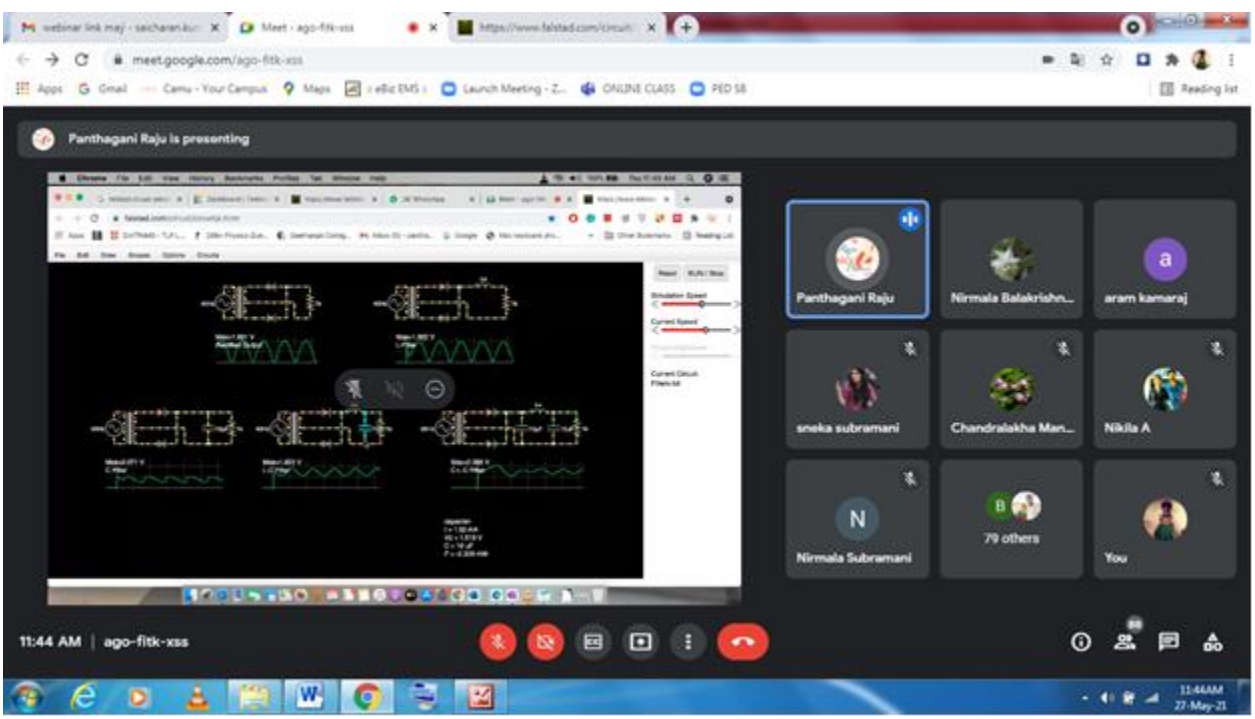
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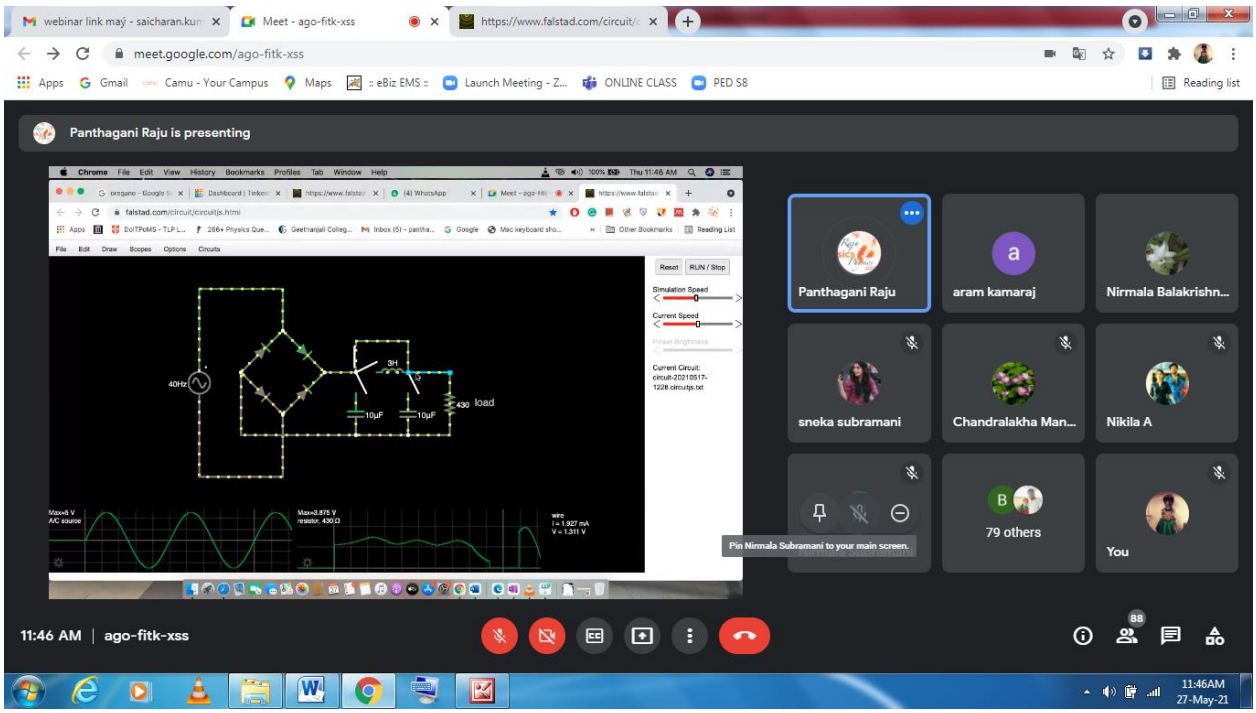
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27-May-21